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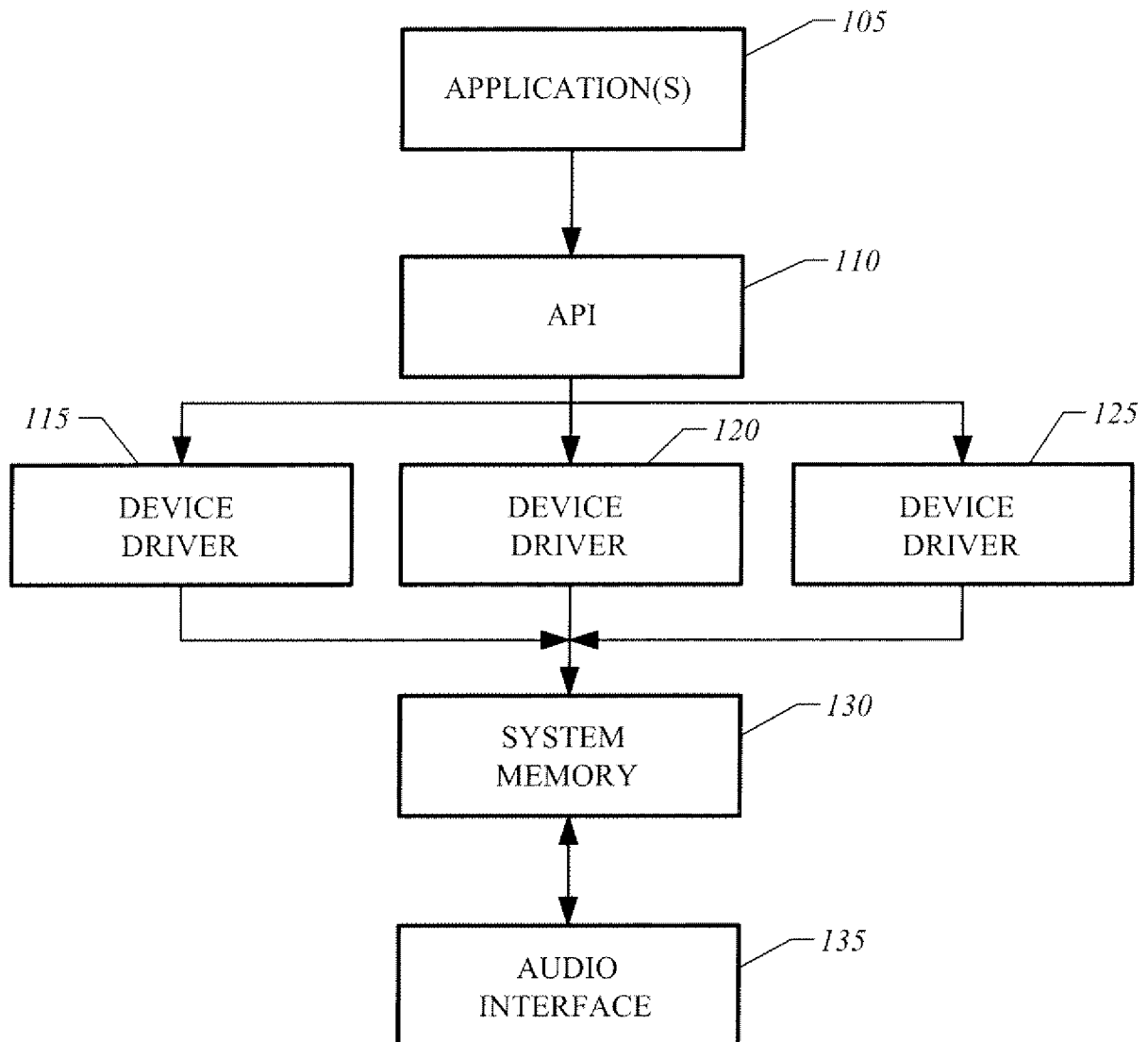


FIG. 1

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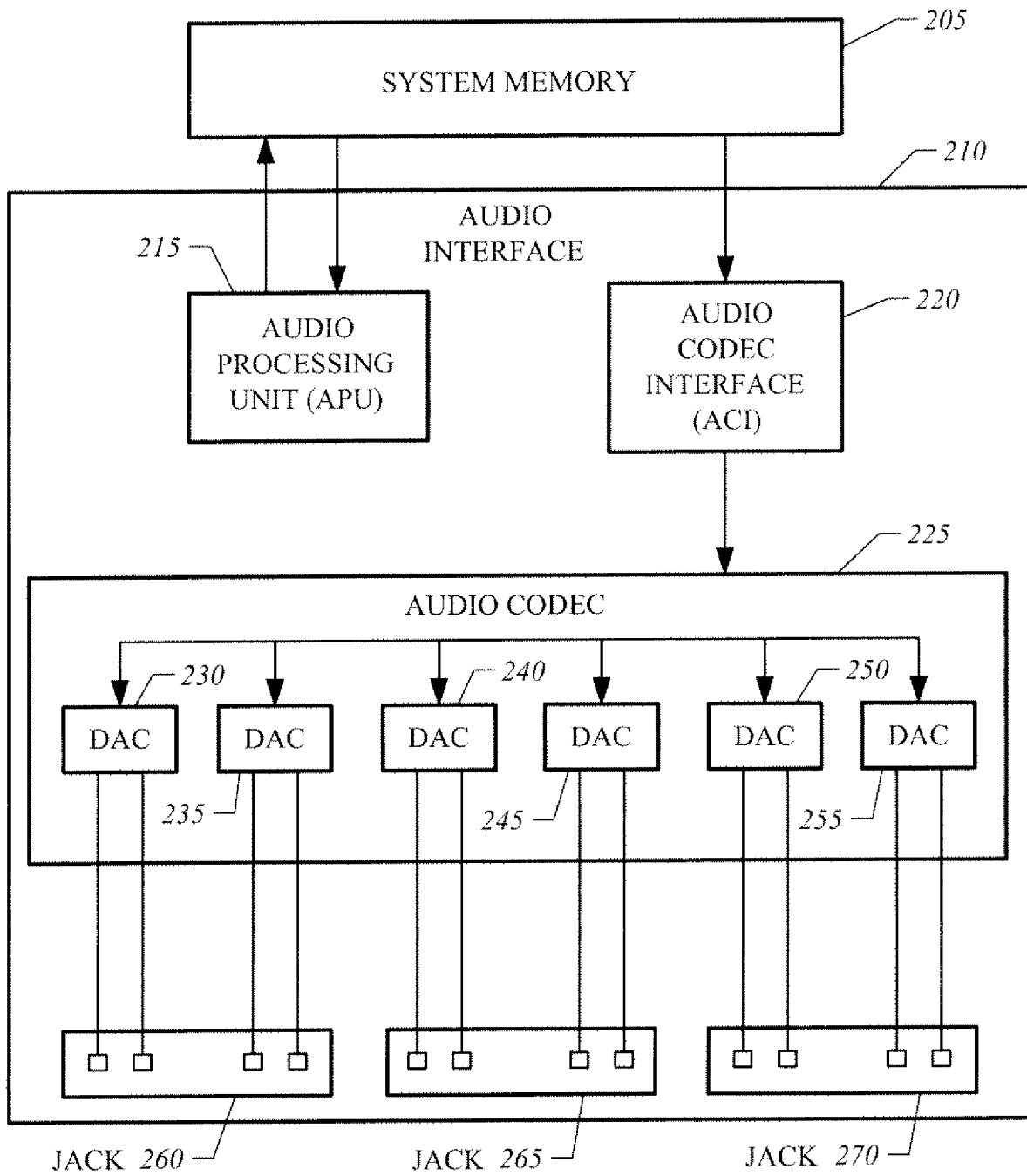


FIG. 2

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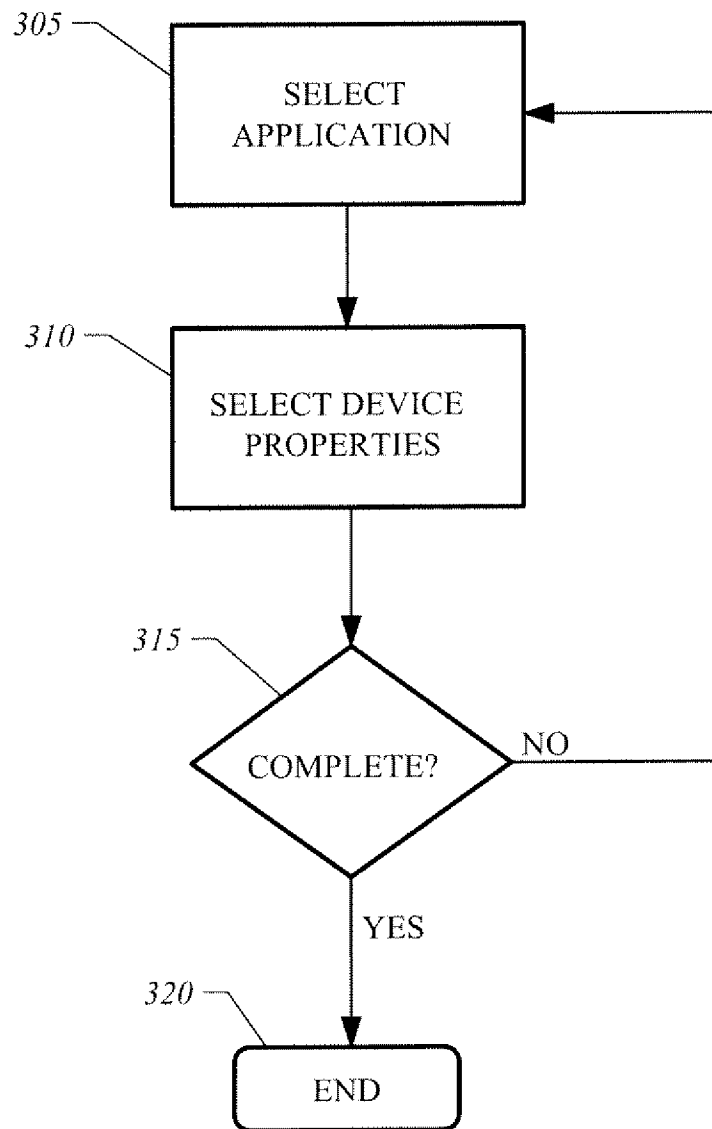
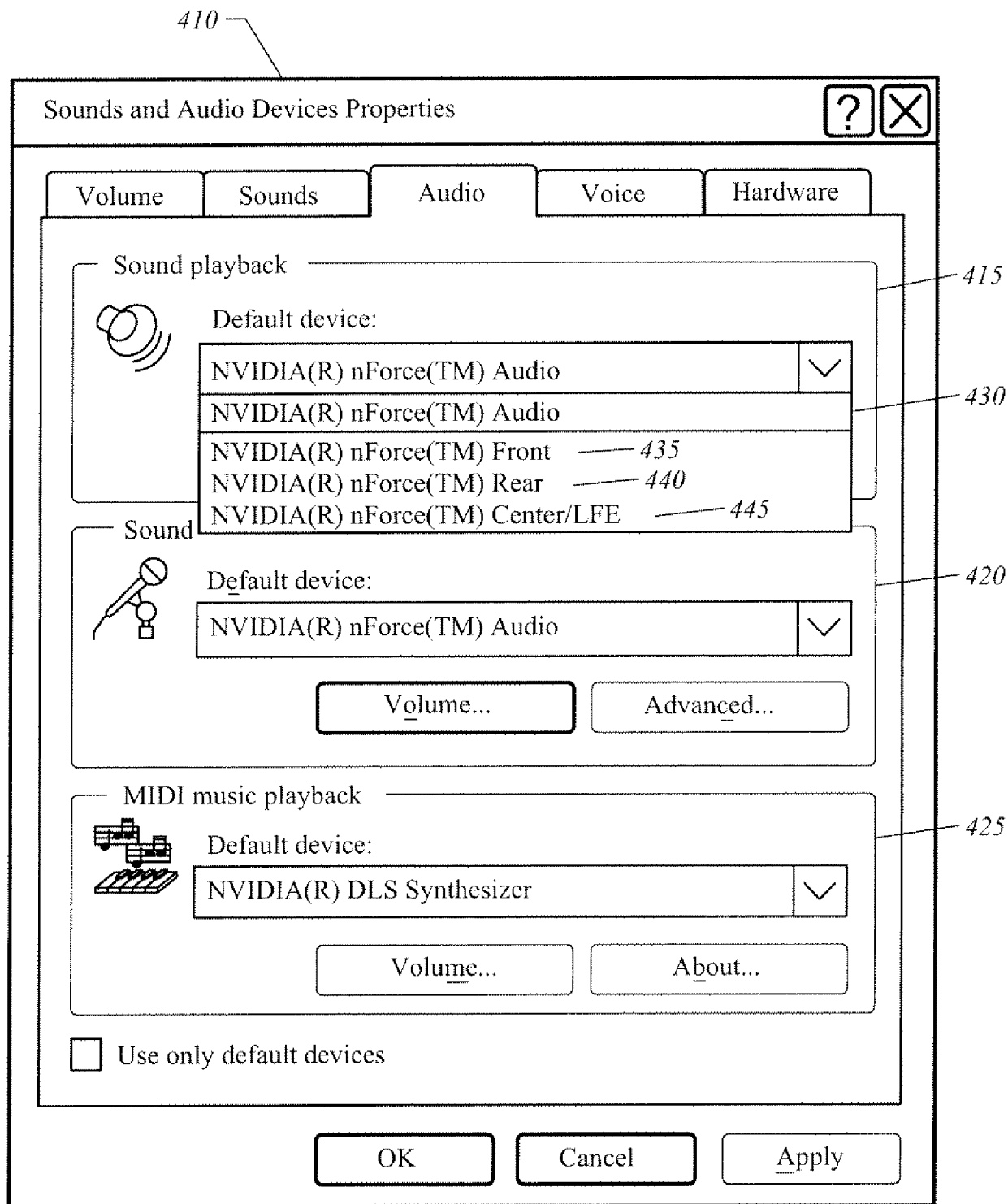


FIG. 3

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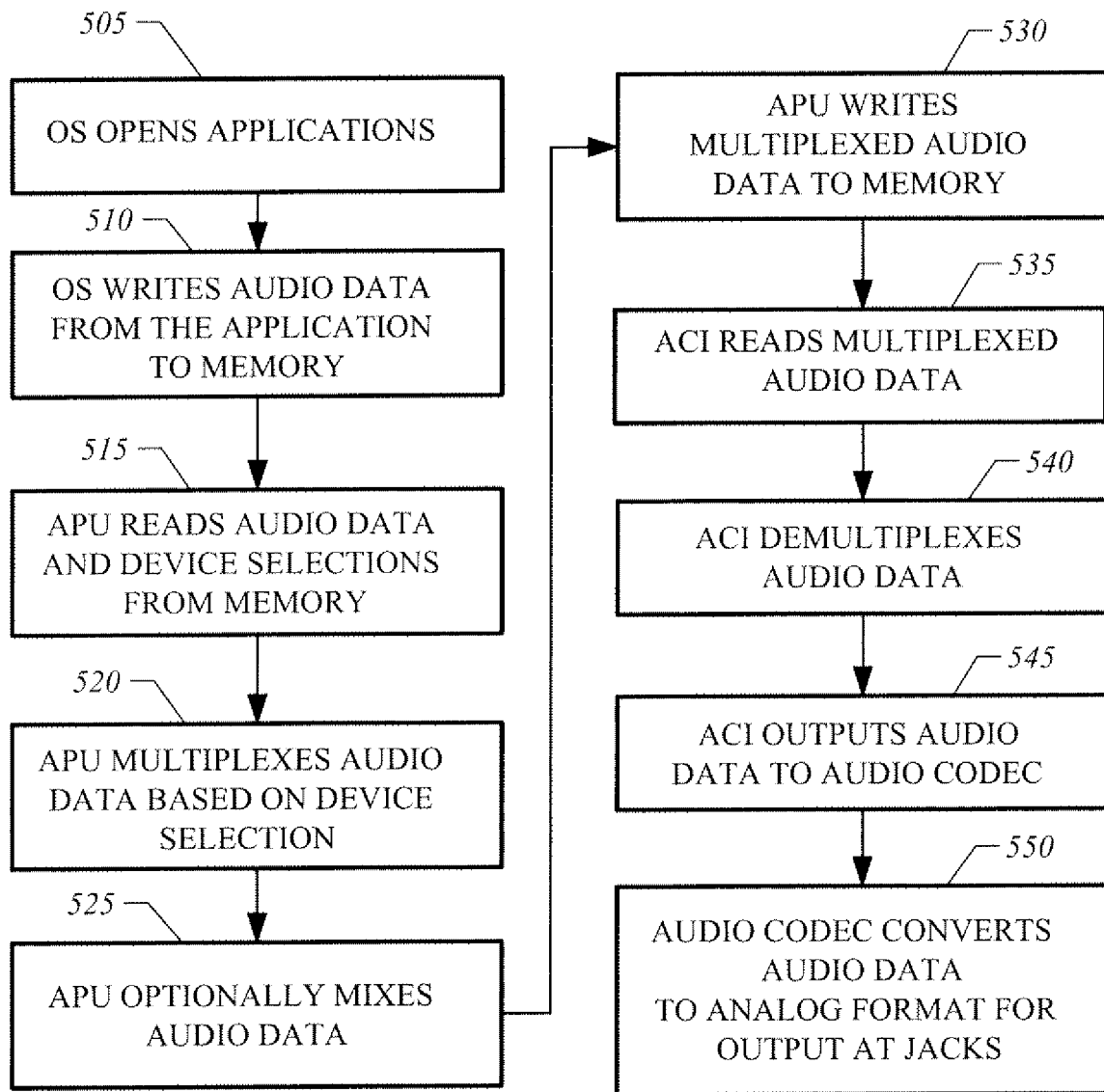


FIG. 5

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APP	DATA	CHANNEL
1	AB/CD/...	01
2	EF/GH/...	45
3	IJ/KL/...	23

FIG. 6A

APU 215 OUTPUT	AB I J EF / CD KL GH
DEVICE SEQUENCE	0 1 2 3 4 5 / 0 1 2 3 4 5

FIG. 6B

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DATA INPUT 1	DATA INPUT 2	OUTPUT
AB/CD/...	EF/GH/...	ZY/VW/...

FIG. 7